1. **EVK Overview**

HPL-EVK V4R2 (EVK) is an evaluation kit designed for testing of high-precision boards and modules, and can be used with UB4B0, UB4B0M, UB482, UM4B0-EXTA, UM482-EXTA etc.

Note: Please make sure the board is installed and all the interfaces are connected normally before powering on the EVK. DO NOT plug/unplug the product when the power is on.



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Figure 1. HPL-EVK Front

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| --- | --- | --- | --- |
| 1. Power Connector | 1. Power Switch | 1. COM1 | 1. CAN |
| 1. COM3 | 1. Reset Button | 1. COM2 | 1. Ethernet Port |
| 1. USB-B Port | 1. SMA Ports (total 6) | 1. Dial Switch | 1. SPI & UART ½ |
| 1. SPDT Switch | 1. NII\_UART0/1 (LVTTL) | 1. NII\_UART0/1 (RS232) |  |

1. **Instructions**

In the same order as in Figure 1.

1. Power Connector – Recommended power range is 9-36V. The power supply above 36V may cause permanent damage to EVK and/or the board.
2. Power Switch – position to the left is OFF.
3. COM1 – use the RS232 connector to link with computer.
4. CAN – above the printed letters JP2  
   JP2 PIN definitions

|  |  |
| --- | --- |
| PIN1 | CANL |
| PIN2 | GND |
| PIN3 | CANH |
| PIN4 | GND |

1. COM3 – use the RS232 connector to link with computer.
2. Reset Button – press to reset the HPL-EVK.
3. COM2 – use the RS232 connector to link with computer.
4. Ethernet Port.
5. USB-B Port.
6. SMA Ports   
   SMA Port PIN definitions (based on the letters nearby\_

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| J5 – EVT1 | J8 – PPS | J21 – FWD | J11 – SPEED | J6 – EVT2 | J20 – PPS2 |

1. Dial Switch  
   PIN1 and PIN2 are used for boards functional selection.

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| --- | --- | --- |
| **PIN2** | **PIN1** | **Functions** |
| 0 | 0 | EVENT2&GPIO |
| 0 | 1 | COM3 |
| 1 | 0 | CAN |
| 1 | 1 | Reserved |

Dial Switch PIN3 for whether the RTK signal is being received.

|  |  |
| --- | --- |
| **PIN3** | **Functions** |
| 0 | RTK\_LED signal is not received |
| 1 | RTK\_LED signal is received |

1. Reserved for SPI & UART ½ connector.
2. SPDT switch – returns to factory settings. Normally should be kept in the right position on OFF.
3. NII\_UART0/1 (LVTTL) – reserved for LVTTL.
4. NII\_UART0/1 (RS232) – reserved for RS232.